

Compal confidential

Schematics Document

LS-9312P Card Reader board

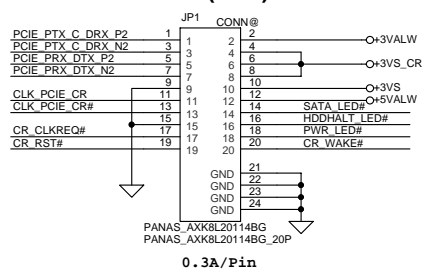
(with USB TX & RX Redriver)

2013-04-16

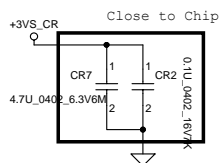
ver: 1.0

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Date: Monday, October 21, 2013				Sheet	1	of	3	Rev	A

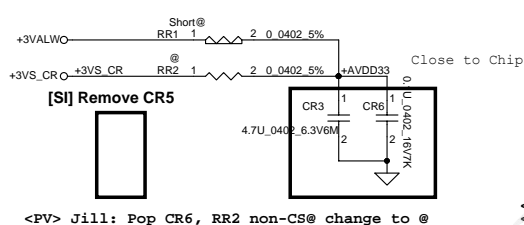
To M/B (BTB)



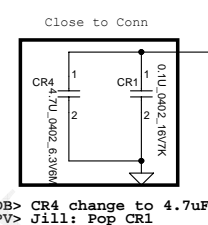
0.3A/Pin



<DB> CR7 change to 4.7uF
<PV> J11: Pop CR2

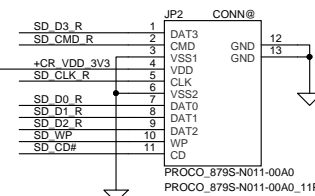


<DB> J11: Pop CR6, RR2 non-CS@ change to @

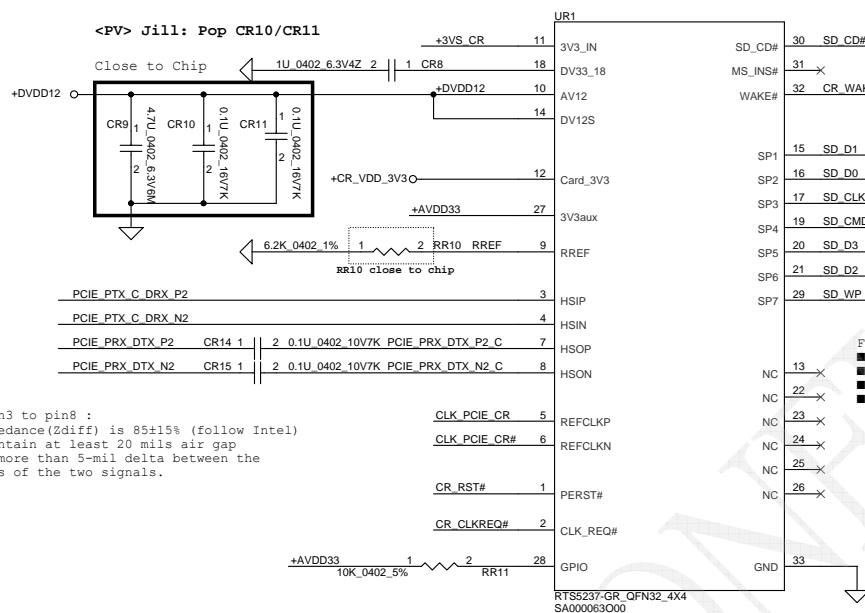


<DB> CR4 change to 4.7uF
<PV> J11: Pop CR1

Card Reader Conn



- Complete D3V3 power plane partition under RTS5227 chip and Socket in Power Layer
- Pin 11(3V3_IN) / pin 12 (CARD_3V3) trace width is 40 mils (minimum).
- Pin 27(3V3aux) trace width is 30 mils (minimum).
- Pin 10(AV12), pin 14(DV12S), pin 18 (DV33_18) trace width is 20 mils (minimum).
- Pin 9 (RREF) trace width is 12 mils (minimum).



For pin3 to pin8 :
1. impedance (Zdiff) is 85±15% (follow Intel)
2. Maintain at least 20 mils air gap
3. No more than 5-mil delta between the lengths of the two signals.

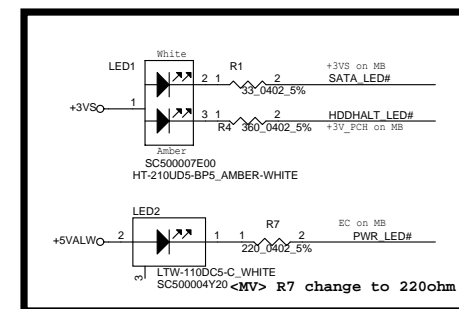
For pin15 to pin21 :
■ CLK - DATA | trace length ≤ 100 mils
■ DATA.x - DATA.y | trace length ≤ 100 mils
■ It is recommended that use a guard ground to isolate SD_CLK trace
■ These traces of characteristic impedance are 50±15%.



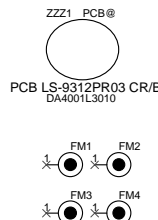
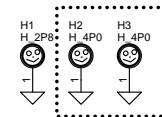
<PV> J11: RR12 non-CS @ change to @

<DB> Add CR_WAKE#

<SI> CR12 change to @ from @EMI@



Stand off



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